IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s):   Monte Mar

Serial No.:   09/909,047   Group Art Unit:

Filed: 07/18/01   Examiner:

Title: A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE (AS AMENDED)

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
[initials]	A	6,144,327	11/07/00	Distinti et al.	341	126	08/12/97
[initials]	B	5,202,687	04/13/93	Distinti	341	158	06/12/91
	C						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
	D							
	E							
	F							

Related Pending US Patent Applications

Examiner Initial	No.	Docket Number, Title, Filing Date, Serial Number & Inventors
[initials]	G	CYPR-CD00174; "PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM (AS AMENDED)"; 08/14/01; 09/930,021; Mar et al.
[initials]	H	CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; W. Snyder

Examiner	[signature]	Date Considered	6/200X

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
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